

IN THE CLAIMS:

1. (Currently Amended) A method, comprising:
 - receiving an input current from a digital to analog converter;
 - mirroring the input current;
 - converting the received input current to a voltage;
 - filtering the voltage; and
 - converting the filtered voltage into an output current using the mirrored input current.
2. (Original) The method of claim 1, wherein the filtering is performed by a low pass filter.
3. (Original) The method of claim 2, wherein the low pass filter includes a third order RC filter.
4. (Original) The method of claim 1, further comprising outputting the output current to a mixer.
5. (Original) The method of claim 1, wherein the converting the received input voltage and the converting the filtered voltage are performed by a first and second MOSFET, respectively.

6. (Original) The method of claim 5, wherein the second MOSFET is the inverse of the first MOSFET.

7. (Original) The method of claim 1, wherein the filtering filters out clocking glitches and quantization noise.

8. (Original) The method of claim 1, wherein the filtering yields a DC gain of one.

9. (Currently Amended) A system, comprising:

a current mirror that mirrors an input current from a digital to analog converter;

a first MOSFET capable of converting the received input current to a voltage;

a filter, communicatively coupled to the first MOSFET, capable of filtering the voltage; and

a second MOSFET, communicatively coupled to the filter and the current mirror, capable of converting the filtered voltage into an output current using the mirrored input current.

10. (Original) The system of claim 9, wherein the filter includes a low pass filter.

11. (Original) The system of claim 10, wherein the low pass filter includes a third order RC filter.
12. (Original) The system of claim 9, further comprising means for outputting the output current to a mixer, the means communicatively coupled to the second MOSFET.
13. (Original) The system of claim 9, wherein the second MOSFET is the inverse of the first MOSFET.
14. (Original) The system of claim 9, wherein the low pass filter filters out clocking glitches and quantization noise.
15. (Original) The system of claim 9, wherein the low pass filter yields a DC gain of one.
16. (Original) A transmitter incorporating the system of claim 9.
17. (Currently Amended) A system, comprising:
means for receiving an input current from a digital to analog converter;
means for mirroring the input current;
means for converting the received input current to a voltage;

means for filtering the voltage; and
means for converting the filtered voltage into an output current using the mirrored
input current.